# Advanced Encryption Standard Algorithm for File Security 

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#### Abstract

The efficacy of the Advanced Encryption Standard (AES) algorithm has made it very attractive for data encryption. As a result of this, it has been employed by many large organizations for safeguarding files in any binary format. It makes use of a symmetric key to achieve successful data encoding. To encrypt a file, it uses different key sizes. Although a bigger key size increases the degree of unpredictability, it also increases the encryption time. AES utilizes Add round key, Byte substitution, Shift rows and Shift column matrix operations together with a Galois Field computation in Modulus two (MOD 2). This paper shows the step-by-step process of how data is encrypted using the AES.


Indexed Terms- AES, Add round key, Byte substitution, Galois Field, Shift rows and Shift column.

## I. INTRODUCTION

AES, also known as Rijndael, is a symmetric block cipher that uses a set block size in its cryptographic method [1]. It employs bits sizes of 128,192 , and 256 , with the following rounds; 10,12 and 14 respectively; it utilizes higher key sizes in recent times of systems with fast processing speed [2]. The AES cryptographic technique is widely used since it is well known for its high security [3]. It encrypts plaintext through the following processes; byte substitution (SubBytes), shift rows, mix column and the Add Round Key [4].

## II. HOW AES WORKS

The AES performs encryption of files via four major steps including; byte substitution, shift rows, mix column and add round key [4]. The operation scrambles the input data using a particular technique
in each step until the required output is generated. This work will concentrate on the encryption of data with a 128-bit input.

## III. ADD ROUND KEY

The input data which is a plaintext of 128 bits (16 bytes or 4 words) is exclusively ORed (XOR) with the cipher key of 128 bits [5]. The XOR logic gate has two inputs, it produces a logic output of "one" only when one of the inputs is a "one", else, zero otherwise. i.e.

0 XOR $0=0$
1 XOR $0=1$
1 XOR $1=0$
0 XOR $1=1$
The hexadecimal presentation of the plaintext and the cipher key are XORed and the intermediate results are stored as an output state [6].

Table 3.1 Hexadecimal computation

| Hex | 8 | 4 | 2 | 1 |
| :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 0 | 0 |
| 1 | 0 | 0 | 0 | 1 |
| 2 | 0 | 0 | 1 | 0 |
| 3 | 0 | 0 | 1 | 1 |
| 4 | 0 | 1 | 0 | 0 |
| 5 | 0 | 1 | 0 | 1 |
| 6 | 0 | 1 | 1 | 0 |
| 7 | 0 | 1 | 1 | 1 |
| 8 | 1 | 0 | 0 | 0 |
| 9 | 1 | 0 | 0 | 1 |
| A | 1 | 0 | 1 | 0 |
| B | 1 | 0 | 1 | 1 |
| C | 1 | 1 | 0 | 0 |
| D | 1 | 1 | 0 | 1 |
| E | 1 | 1 | 1 | 0 |
| F | 1 | 1 | 1 | 1 |

The hexadecimal representation of the plain text is XORed with the hexadecimal representation of the [6]. Where $\bigotimes$ represents the symbol for XOR operation

| 32 | 88 | 31 | $e 0$ |
| :--- | :--- | :--- | :--- |
| 43 | $5 a$ | 31 | 37 |
| $f 6$ | 30 | 98 | 07 |
| $a 8$ | $8 d$ | $a 2$ | 34 |

Block of Plain

| 2b | 28 | ab | 09 |
| :---: | :---: | :---: | :---: |
| $7 e$ | ae | f7 | ef |
| 15 | d2 | 15 | $4 f$ |
| 16 | a6 | 88 | $3 c$ |

Block of Cipher

| 0011 | 1000 | 0011 | 1110 |
| :--- | :--- | :--- | :--- |
| 0010 | 1000 | 0001 | 0000 |
| 0100 | 0101 | 0011 | 0011 |
| 0011 | 1010 | 0001 | 0111 |
| 1111 | 0011 | 1001 | 0000 |
| 0110 | 0000 | 1000 | 1111 |
| 0101 | 1000 | 1010 | 0011 |
| 1000 | 1101 | 0010 | 0100 |

Hexadecimal representation of plain text

| 0010 | 0010 | 1010 | 0000 |
| :--- | :--- | :--- | :--- |
| 1011 | 1000 | 1011 | 1001 |
| 0111 | 1010 | 1111 | 1100 |
| 1110 | 1110 | 0111 | 1111 |
| 0001 | 1101 | 0001 | 0100 |
| 0101 | 0010 | 0101 | 1111 |
| 0001 | 1010 | 1000 | 0011 |
| 0110 | 0110 | 1000 | 1100 |

Hexadecimal representation of Cipher text

Table 3.2 Output state of Add round
Hexadecimal representation of add
round key

| 0001 | 1010 | 1001 | 1110 |
| :--- | :--- | :--- | :--- |
| 1001 | 0000 | 1001 | 1001 |
| 0011 | 1111 | 1100 | 1111 |
| 1101 | 0110 | 0110 | 1000 |
| 1110 | 1110 | 1000 | 0100 |
| 0011 | 0010 | 1101 | 1000 |
| 1010 | 0010 | 0010 | 0000 |
| 1110 | 1011 | 1010 | 1000 |


| 19 | $a 0$ | $9 a$ | $e 9$ |
| :---: | :---: | :---: | :---: |
| $3 d$ | $f 4$ | $c 6$ | $\mathrm{f8}$ |
| e 3 | e 2 | 8 d | 48 |
| $b e$ | $2 b$ | $2 a$ | 08 |

## IV. SUBSTITUTION BYTE

During this stage, values of each output are replaced with values from the AES S-Box lookup table that correspond to it [6].

Table 4.1 S-Box [7][8]


The first two numbers obtained from the hexadecimal computation of the add round key are 1 and 9. From the AES S-box lookup table, 1 is read from the X -axis, while 9 is read from the Y -axis. The value at the point of intersection from the lookup table becomes the new value. This step is repeated for all values gotten from the output of the add round key table until a new output state known as byte substitution is obtained [9][10].

| 19 | a0 | 9 a | e9 |
| :---: | :---: | :---: | :---: |
| 3 d | f4 | c6 | f8 |
| e3 | e2 | $8 d$ | 48 |
| be | $2 b$ | 2 a | 08 |

Table 4.1 Output state after Byte substitution

| d4 | e0 | b8 | 1e |
| :--- | :--- | :--- | :--- |
| 27 | bf | b4 | 41 |
| 11 | 98 | $5 d$ | 52 |
| ae | f1 | e5 | 30 |

## V. SHIFT ROWS

The process here entails putting the byte substitution's output in a square matrix $(4 \times 4)$ and then perform a round shift on each row, starting from the second row, the last byte (41) is shifted once to the left to display (b4). While the last bytes on the third and fourth row are shifted two and three times respectively. [11].

| d 4 | e 0 | b 8 | 1 e |
| :---: | :---: | :---: | :---: |
| 27 | bf | b 4 | 41 |
| 11 | 98 | 5 d | 52 |


| d 4 | e 0 | b 8 | 1 e |
| :---: | :---: | :---: | :---: |
| bf | b 4 | 41 | 27 |
| 5 d | 52 | 11 | 98 |

ae f1 e5 30
30 ae f1 e5
VI. MIX COLUMN

This is accomplished by multiplying the shift rows output by a predetermined $4 \times 4$ matrix.

| 02 | 03 | 01 | 01 |
| :--- | :--- | :--- | :--- |
| 01 | 02 | 03 | 01 |
| 01 | 01 | 02 | 03 |
| 03 | 01 | 01 | 02 |

$4 \times 4$ Predefined Matrix [12]

Each word of the Shift rows output state ( $4 \times 1$ matrixes) is multiplied by the predefined $4 \times 4$ matrix. Let $\mathrm{W}_{0}=(\mathrm{d} 4 \mathrm{bf} 5 \mathrm{~d} 30) \mathrm{W}_{1}=(\mathrm{e} 0 \mathrm{~b} 452 \mathrm{ae}) \mathrm{W}_{2}=(\mathrm{b} 841$ $11 \mathrm{f} 1)$ and $\mathrm{W}_{3}=\left(1 \mathrm{e} 2798\right.$ e5). Where $\mathrm{W}_{0}, \mathrm{~W}_{1}, \mathrm{~W}_{2}$, and $\mathrm{W}_{3}$ represents each word of the shift rows output state respectively.
This is done using the Galois fields which is computing polynomials as bits sequence, also the Galois fields are computed in Modulus two (MOD 2), so the values obtained from the computations will either be 0 s and/or 1 s . Galois field is given as:
$\mathrm{A} \in \mathrm{GF}\left(2^{8}\right)$ [13]
$\mathrm{A}(x)=a 7 x^{7}+\mathrm{a} 6 \mathrm{x}^{6}+\mathrm{a} 5 \mathrm{x}^{5}+\mathrm{a} 4 \mathrm{x}^{4}+\mathrm{a} 3 \mathrm{x}^{3}+\mathrm{a} 2 \mathrm{x}^{2}+a 1 x+a 0, \quad a i$ $\in G F(2)=\{0,1\}$. [13]

If the product of two Galois fields is more than a byte ( 8 bits) then the result can be reduced by performing XOR operation on a special primitive polynomial known as the irreducible polynomial. This polynomial is given as: $\mathrm{P}(\mathrm{x})=\mathrm{x}^{8}+\mathrm{x}^{4}+\mathrm{x}^{3}+\mathrm{x}+1$ [13]

An example is used to illustrate this, by using the output state obtained after the shift rows operation. The first word is multiplied by a $4 \times 4$ predefined matrix.


| 1101 |
| :--- |
| 0110 |
| 1011 |
| 1111 |
| 0101 |
| 1101 |
| 0011 |
| 0000 |

Applying Galois field $(\mathrm{A}(x) \quad=$ $\left.a 7 x^{7}+\mathrm{a} 6 \mathrm{x}^{6}+\mathrm{a} 5 \mathrm{x}^{5}+\mathrm{a} 4 \mathrm{x}^{4}+\mathrm{a} 3 \mathrm{x}^{3}+\mathrm{a} 2 \mathrm{x}^{2}+a 1 x+a 0\right)$ we have: $\left(X^{1}\left(x^{7}+x^{6}+x^{4}+x^{2}\right)\right)+\left(x^{1}+x^{0}\left(x^{7}+x^{5}+x^{4}+x^{3}+x^{2}\right.\right.$ $\left.\left.+x^{1}+x^{0}\right)\right)+\left(X^{0}\left(x^{6}+x^{4}+x^{3}+x^{2}+x^{0}\right)\right)+\left(X^{0}\left(x^{5}+x^{4}\right)\right)$

Representing the above expression in its binary equivalent we have:
$(10 * 11010100)+(11 * 10111111)+(01 * 0101$
$1101)+(01 * 00110000)$
i.e. $\left(X^{1}\left(x^{7}+x^{6}+x^{4}+x^{2}\right)\right)=(10 * 11010100),\left(x^{1}+x^{0}\right.$
$\left.\left(x^{7}+x^{5}+x^{4}+x^{3}+x^{2}+x^{1}+x^{0}\right)\right)=(11 * 1011$ 1111 $)$,
$\left(\mathrm{X}^{0}\left(\mathrm{x}^{6}+\mathrm{x}^{4}+\mathrm{x}^{3}+\mathrm{x}^{2}+\mathrm{x}^{0}\right)\right)=\left(01 * 0101\right.$ 1101), $\left(\mathrm{X}^{0}\left(\mathrm{x}^{5}\right.\right.$ $\left.\left.+x^{4}\right)\right)=(01 * 00110000)$
Solving the polynomial for the first byte of the first word $\left(\mathrm{W}_{0}\right)$ we have
$\left(\mathrm{X}^{1}\left(\mathrm{x}^{7}+\mathrm{x}^{6}+\mathrm{x}^{4}+\mathrm{x}^{2}\right)\right)=(10 * 11010100)$;

|  | 1 |
| :--- | :--- |
| 2 | 3 |
| 4 | 5 |
| 6 | 7 |
| 7 | 8 |

Answer $=1 X^{8}+1 X^{7}+0 X^{6}+1 X^{5}+0 X^{4}+1 X^{3}+0 X^{2}$ $+0 X^{1}+0 X^{0}=110101000$
Since $6,4,2$ and 0 are not captured in the answer, they are replaced with 0 s in the binary equivalent while the captured answers are replaced with 1 s respectively.

Because the product of the Galois fields is more than a byte ( 8 bits), the result is reduced by performing XOR operation with the irreducible polynomial.


Similarly solving the polynomial for the second byte of the first word $\left(\mathrm{W}_{0}\right)$ we have:
$\left(\mathrm{x}^{1}+\mathrm{x}^{0}\left(\mathrm{x}^{7}+\mathrm{x}^{5}+\mathrm{x}^{4}+\mathrm{x}^{3}+\mathrm{x}^{2}+\mathrm{x}^{1}+\mathrm{x}^{0}\right)\right)=(11 * 1011$ 1111)

|  | 0 | 1 |
| :--- | :--- | :--- |
| 0 | 0 | 1 |
| 1 | 1 | 2 |
| 2 | 2 | 3 |
| 3 | 3 | 4 |
| 4 | 4 | 5 |
| 5 | 5 | 6 |
| 7 | 7 | 8 |

Answer $=1 x^{8}+1 \mathrm{x}^{7}+1 \mathrm{x}^{6}+0 \mathrm{x}^{5}+0 \mathrm{x}^{4}+0 \mathrm{x}^{3}+0 \mathrm{x}^{2}+0 \mathrm{x}^{1}$ $+0 x^{0}=111000001$
In computing the answer, we use XOR addition. Anywhere a number in the answers appear as an even number we replace it with 0 s. For odd appearances, we replace with 1 s. i.e. $8,7,6$ and 0 appeared once therefore we replace them with 1 s while the rest appeared twice therefore we denote them as 0s.
In addition, since the binary value doesn't fit into a byte, we reduce it with $\mathrm{x}^{8}+\mathrm{x}^{4}+\mathrm{x}^{3}+\mathrm{x}^{1}+\mathrm{x}^{0}=$ 100011011.

```
111000001
100011011
011011010=1101 1010
```

Similarly solving the polynomial for the third byte of the first word $\left(\mathrm{W}_{0}\right)$ we have:
$\left(\mathrm{X}^{0}\left(\mathrm{x}^{6}+\mathrm{x}^{4}+\mathrm{x}^{3}+\mathrm{x}^{2}+\mathrm{x}^{0}\right)\right)=(01 * 0101$ 1101 $)$
$(01 * 01011101)=01011101$
Solving the polynomial for the third byte of the first word $\left(\mathrm{W}_{0}\right)$ we have:
$\left(\mathrm{X}^{0}\left(\mathrm{x}^{5}+\mathrm{x}^{4}\right)\right)=(01 * 00110000)$
$(01 * 00110000)=00110000$
Performing XOR operation on the first word the following answer is obtained;
$\left(\mathrm{X}^{1}\left(\mathrm{x}^{7}+\mathrm{x}^{6}+\mathrm{x}^{4}+\mathrm{x}^{2}\right)\right)=(10 * 11010100),\left(\mathrm{x}^{1}+\mathrm{x}^{0}\left(\mathrm{x}^{7}\right.\right.$
$\left.\left.+x^{5}+x^{4}+x^{3}+x^{2}+x^{1}+x^{0}\right)\right)=(11 * 10111111),\left(X^{0}\left(x^{6}\right.\right.$
$\left.\left.+x^{4}+x^{3}+x^{2}+x^{0}\right)\right)=(01 * 01011101),\left(X^{0}\left(x^{5}+x^{4}\right)\right)$
$=(01 * 00110000)$

```
10110011
1 1 0 1 1 0 1 0
01011101
0 0 1 1 0 0 0 0
0000 0100=04
```

The same process is repeated for the second word $\left(\mathrm{W}_{1}\right)$, third word $\left(\mathrm{W}_{2}\right)$ and fourth word $\left(\mathrm{W}_{3}\right)$ and the following result is obtained as the output state

| 04 | e 0 | 48 | 28 |
| :--- | :--- | :--- | :--- |
| 66 | cb | f 8 | 06 |
| 81 | 19 | d 3 | 26 |
| e 5 | 9 a | 7 a | 4 c |

## VII. ADD ROUND KEY

The processes done here is the same with that in section 2, in this process, output state from the Mix column operation is exclusively ORed (XOR) with the 128 bits ( 16 bytes or 4 words) cipher key.

| 04 | e 0 | 48 | 28 |  |
| :---: | :---: | :---: | :---: | :---: |
| 66 | cb | f 8 | 06 |  |
| 81 | 19 | d 3 | 26 |  |
| e5 | 9 a | 7 a | 4 c |  |
| Mix Column |  |  |  |  |

Output

| a0 | 88 | 23 | 2 a |
| :---: | :---: | :---: | :---: |
| fa | 54 | a 3 | 6 c |
| fe | 2 c | 39 | 76 |
| 17 | b 1 | 39 | 05 |

Cipher Key

| a4 | 68 | $6 b$ | 02 |
| :---: | :---: | :---: | :---: |
| $9 c$ | $9 f$ | $5 b$ | $b a$ |
| $7 f$ | 35 | ea | 50 |
| f2 | $2 b$ | 43 | 49 |

Output State

Table 6.1 Output state from Add round key

| a4 | 68 | $6 b$ | 02 |
| :---: | :---: | :---: | :---: |
| 9 c | 9 f | 5 b | 6 a |
| 7 f | 35 | ea | 50 |
| f2 | 2 b | 43 | 49 |

Except for the mix column, which is performed nine times, the entire process is done ten times. Following that, the resulting AES-encrypted file is acquired.

## CONCLUSION

AES is a trusted algorithm for securing files, which is why it is employed by notable organizations for securing documents. This paper has therefore broken the processes required by this algorithm to secure information into various components in a bid to demonstrate why it is still one of the most reliable algorithm for protecting data.

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## REFERENCES

[1] M. N. Islam, M. M. H. Mia, M. F. I. Chowdhury and M. A. Matin, "Effect of Security Increment to Symmetric Data Encryption through AES Methodology," 2008 Ninth ACIS International Conference on Software Engineering, Artificial Intelligence, Networking, and Parallel/Distributed Computing, Phuket, 2008, pp. 291-294, doi: 10.1109/SNPD.2008.101.
[2] M. Panda, "Performance analysis of encryption algorithms for security," 2016 International Conference on Signal Processing, Communication, Power and Embedded System (SCOPES), Paralakhemundi, 2016, pp. 278-284, doi: 10.1109/SCOPES.2016.7955835.
[3] NIST, "Advanced Encryption Standard (AES)", FIPS PUBS 197, National Institute of Standards and Technology, November 2001.
[4] T. P. Innokentievich and M. V. Vasilevich, "The Evaluation of the cryptographic strength of asymmetric encryption algorithms," 2017 Second Russia and Pacific Conference on Computer Technology and Applications (RPC), Vladivostok, 2017, pp. 180-183, doi: 10.1109/RPC.2017.8168094.
[5] A. Joshi, P. K. Dakhole and A. Thatere, "Implementation of S-Box for Advanced

Encryption Standard," 2015 IEEE International Conference on Engineering and Technology (ICETECH), Coimbatore, 2015, pp. 1-5, doi: 10.1109/ICETECH. 2015.7275043.
[6] A. Kak, "Lecture 8: AES: The Advnced Encryption Standard, Lecturer Notes on Computer and network Security", February 11, 2021
[7] A. Joshi, P. K. Dakhole and A. Thatere, "Implementation of S-Box for Advanced Encryption Standard," 2015 IEEE International Conference on Engineering and Technology (ICETECH), 2015, pp. 1-5, doi: 10.1109/ICETECH.2015.7275043.
[8] Shreenivas Pai N, Raghuram S, Chennakrishna M and A. S. V. Karthik, "Logic optimization of AES S-Box," 2016 International Conference on Automatic Control and Dynamic Optimization Techniques (ICACDOT), 2016, pp. 1042-1046, doi: 10.1109/ICACDOT.2016.7877745.
[9] O. B. Sahoo, D. K. Kole and H. Rahaman, "An Optimized S-Box for Advanced Encryption Standard (AES) Design," 2012 International Conference on Advances in Computing and Communications, 2012, pp. 154-157, doi: 10.1109/ICACC.2012.35.
[10] Jingmei Liu, Baodian Wei, Xiangguo Cheng and Xinmei Wang, "An AES S-box to increase complexity and cryptographic analysis," 19th International Conference on Advanced Information Networking and Applications (AINA'05) Volume 1 (AINA papers), 2005, pp. 724-728 vol.1, doi: 10.1109/AINA.2005.84.
[11] Sujatha Hiremath and M.S. Suma, "Advanced Encryption Standard Implemented on FPGA," 2009 Second International Conference on Computer and Electrical Engineering, 7695-3925-6/09 \$26.00 © 2009 IEEE, DOI 10.1109/ICCEE.2009.231
[12] A. Bhardwaj and S. Som, "Study of different cryptographic technique and challenges in future," 2016 International Conference on Innovation and Challenges in Cyber Security (ICICCS-INBUSH), Noida, 2016, pp. 208-212, doi: 10.1109/ICICCS.2016.7542353.
[13] C. Yu and M. Ciesielski, "Formal Analysis of Galois Field Arithmetic Circuits-Parallel

Verification and Reverse Engineering," in IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 38, no. 2, pp. 354-365, Feb. 2019, doi: 10.1109/TCAD.2018.2808457.

